

Notice of References Cited	Application/Control No. 10/697,626		Applicant(s)/Patent Under Reexamination STANLEY, GERALD R.	
	Examiner Devona E. Faulk		Art Unit 2615	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,598,480	01-1997	Kim, Man H.	381/99
*	B	US-5,097,223	03-1992	Alexander, Mark A. J.	330/126
*	C	US-4,751,738	06-1988	Widrow et al.	381/313
*	D	US-5,533,135	07-1996	Gary, Phillip A.	381/99
*	E	US-4,670,709	06-1987	Iredale, John	324/542
*	F	US-2004/0101153	05-2004	Grudin et al.	381/357
*	G	US-6,381,334	04-2002	Alexander, Eric	381/99
*	H	US-4,504,704	03-1985	Ohyaba et al.	381/332
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Sedra/Smith. Microelectronic Circuits. Oxford University Press, 1998. New York, pages 668-671, Figure 8.1
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.